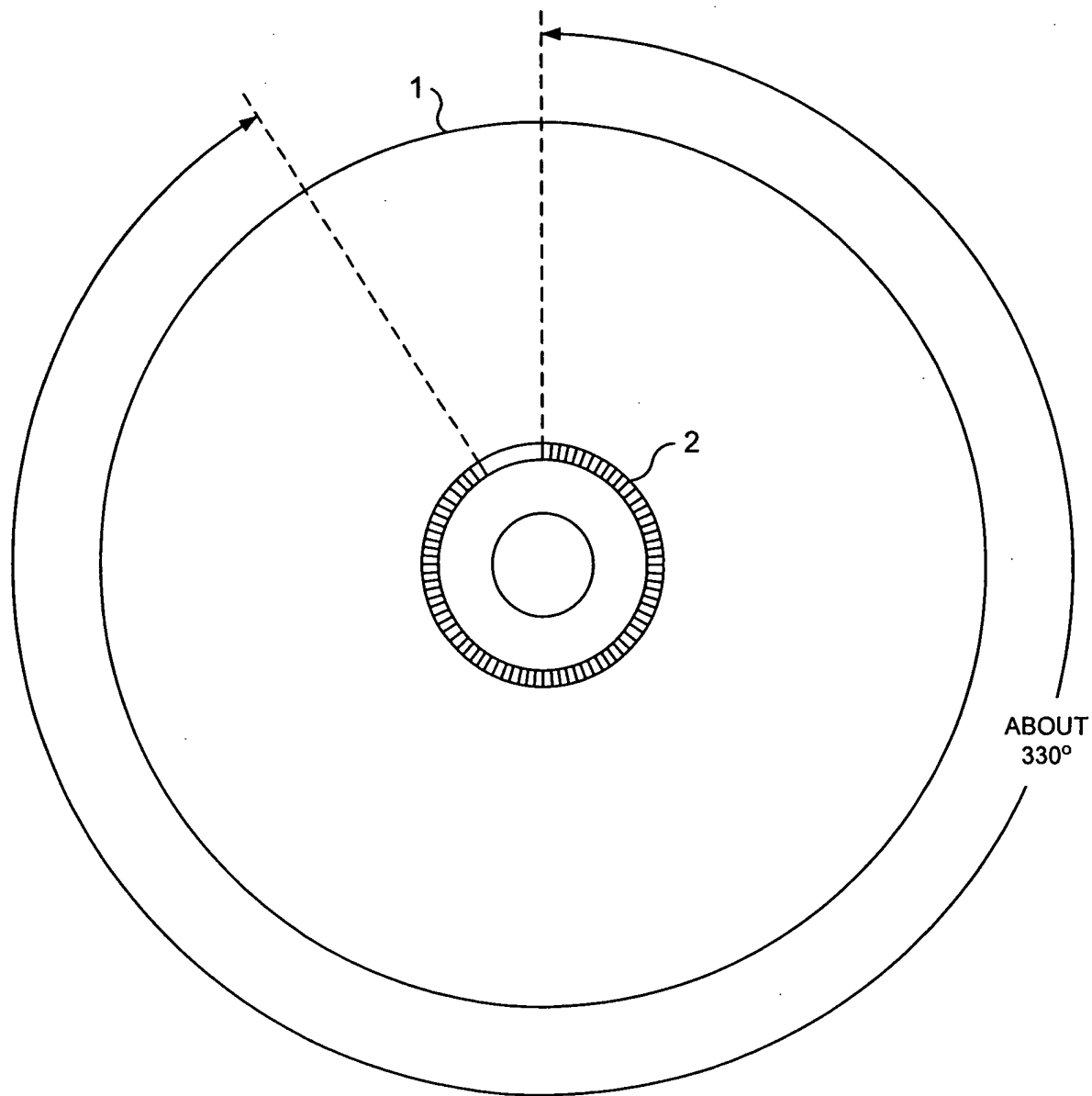




FIG. 1



(PRIOR ART)



FIG. 2

5 bytes					
1 byte	4 bytes				
SB _{BCA}	BCA – Preamble (All 00h)				1 row
RS _{BCA1}	I ₀	I ₁	I ₂	I ₃	4n rows (1≤n≤12)
RS _{BCA1}	I ₄	I ₅	I ₆	I ₇	
RS _{BCA1}	:	:	:	:	
RS _{BCA1}	:	:	:	:	
RS _{BCA2}	:	:	:	:	
:					
:					
RS _{BCAi-i}					
RS _{BCAi}					
RS _{BCAi}	Information				
RS _{BCAi}					
RS _{BCAi}					
RS _{BCAi+i}					
:					
:					
RS _{BCAn-i}					
RS _{BCAn}	:	:	:	:	
RS _{BCAn}	:	:	:	:	
RS _{BCAn}	I _{16n-8}	I _{16n-7}	I _{16n-6}	I _{16n-5}	
RS _{BCAn}	EDC _{BCA} (4 bytes)				
RS _{BCA13}	C _{0.0}	C _{1.0}	C _{2.0}	C _{3.0}	4 rows
RS _{BCA13}	:	:	ECC _{BCA}	:	
RS _{BCA13}	:	:	:	:	
RS _{BCA13}	C _{0.3}	C _{1.3}	C _{2.3}	C _{3.3}	
RS _{BCA14}	BCA – Postamble (All 55h)				1 row
RS _{BCA15}					

(PRIOR ART)